

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>61</b>	("20020114143" "20010040793" "6281577" "6212076" "6157541" "6021048" "5708568" "5448511" "5382829" "5363275" "5345205" "5335145" "5313416" "5290971" "5252857" "5205741" "5117282" "4567543" "3873889" "6292370" "6256203" "6208521" "6137691" "6061245" "6016253" "5805422" "5754409" "5748448" "5717556" "5519578" "5452182" "5220488" "5204806" "5008496" "4990948" "4958260" "4928206" "4781601" "4495546" "4426689" "3971127" "3819989" "6195261" "5825634" "5394303" "6392162" "6384890" "6366473" "5764497" "3499220" "3242384" "6111756" "5224023" "4742183" "20010008306" "6504244" "6462412" "6225688" "6121676" "5220198"	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>3</b>	("5652608"   "6208521"   "6225688").PN.	<b>USP AT</b>
<b>3</b>	<b>L3</b>	<b>2</b>	<b>1 and (antenna\$1 coil\$1)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>4</b>	<b>L4</b>	<b>5</b>	<b>1 and paper</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>1199</b>	<b>428/211.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L7</b>	<b>1</b>	<b>5 and (antenna\$1 and (capacitor\$1 condenser\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L6</b>	<b>67</b>	<b>5 and (antenna\$1 capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>8</b>	<b>L8</b>	<b>67</b>	<b>6 and paper</b>	<b>USP AT; US-P GPU B</b>
<b>9</b>	<b>L9</b>	<b>925</b>	<b>(361/749,750,751).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L10</b>	<b>710</b>	<b>174/254.ccls.</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>11</b>	<b>L11</b>	<b>1388</b>	<b>9 10</b>	<b>USP AT; US-P GPU B</b>
<b>12</b>	<b>L12</b>	<b>124</b>	<b>11 and paper</b>	<b>USP AT; US-P GPU B</b>
<b>13</b>	<b>L13</b>	<b>13</b>	<b>12 and ((antenna\$1 coil\$1) and (condenser\$1 capacitor\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>14</b>	<b>L14</b>	<b>111</b>	<b>12 not 13</b>	<b>USP AT; US-P GPU B</b>
<b>15</b>	<b>L15</b>	<b>106</b>	<b>14 not 1</b>	<b>USP AT; US-P GPU B</b>
<b>16</b>	<b>L16</b>	<b>43</b>	<b>11 and ((antenna\$1 coil\$1) and (condenser\$1 capacitor\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>17</b>	<b>L17</b>	<b>309</b>	<b>11 and (antenna\$1 coil\$1 condenser\$1 capacitor\$1)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>18</b>	<b>L18</b>	<b>236</b>	<b>17 not (12 16)</b>	<b>USP AT; US-P GPU B</b>
<b>19</b>	<b>L19</b>	<b>546</b>	<b>11 and (fold\$4 bend\$4)</b>	<b>USP AT; US-P GPU B</b>
<b>20</b>	<b>L20</b>	<b>20</b>	<b>11 and crease\$1</b>	<b>USP AT; US-P GPU B</b>
<b>21</b>	<b>L21</b>	<b>548</b>	<b>19 20</b>	<b>USP AT; US-P GPU B</b>
<b>22</b>	<b>L22</b>	<b>351</b>	<b>21 not (1 12 16 18)</b>	<b>USP AT; US-P GPU B</b>
<b>23</b>	<b>L23</b>	<b>1306</b>	<b>(174/256,258,259).cccls.</b>	<b>USP AT; US-P GPU B</b>
<b>24</b>	<b>L24</b>	<b>185</b>	<b>23 and paper</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>25</b>	<b>L25</b>	<b>167</b>	<b>24 not (1 12 16 18 22)</b>	<b>USP AT; US-P GPU B</b>
<b>26</b>	<b>L26</b>	<b>22</b>	<b>25 and (fold\$4 bend\$4 crease\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>27</b>	<b>L27</b>	<b>71</b>	<b>25 and (flex flexible)</b>	<b>USP AT; US-P GPU B</b>
<b>28</b>	<b>L28</b>	<b>81</b>	<b>26 27</b>	<b>USP AT; US-P GPU B</b>
<b>29</b>	<b>L29</b>	<b>1</b>	<b>5907477.pn.</b>	<b>USP AT; US-P GPU B</b>
<b>30</b>	<b>L31</b>	<b>0</b>	<b>29 and (fold\$4 bend\$4 crease\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>31</b>	<b>L30</b>	<b>1</b>	<b>29 and (flex flexible)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>32</b>	<b>L32</b>	<b>220</b>	<b>438/110.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>33</b>	<b>L33</b>	<b>60</b>	<b>32 and (flex flexible)</b>	<b>USP AT; US-P GPU B</b>
<b>34</b>	<b>L34</b>	<b>43</b>	<b>32 and (fold\$4 bend\$4 crease\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>35</b>	<b>L35</b>	<b>80</b>	<b>33 34</b>	<b>USP AT; US-P GPU B</b>
<b>36</b>	<b>L36</b>	<b>639</b>	<b>438/107.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>37</b>	<b>L37</b>	<b>190</b>	<b>36 and (flex flexible)</b>	<b>USP AT; US-P GPU B</b>
<b>38</b>	<b>L38</b>	<b>113</b>	<b>36 and (fold\$4 bend\$4 crease\$1)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>39</b>	<b>L39</b>	<b>249</b>	<b>37 38</b>	<b>USP AT; US-P GPU B</b>
<b>40</b>	<b>L40</b>	<b>233</b>	<b>39 not (1 12 16 18 22 28 35)</b>	<b>USP AT; US-P GPU B</b>
<b>41</b>	<b>L41</b>	<b>865</b>	<b>29/830.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>42</b>	<b>L42</b>	<b>370</b>	<b>41 and (flex flexible fold\$4 bend\$4 crease\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>43</b>	<b>L43</b>	<b>21</b>	<b>42 and batch</b>	<b>USP AT; US-P GPU B</b>
<b>44</b>	<b>L44</b>	<b>349</b>	<b>42 not 43</b>	<b>USP AT; US-P GPU B</b>
<b>45</b>	<b>L45</b>	<b>320</b>	<b>44 not (1 12 16 18 22 28 35 40)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>46</b>	<b>L46</b>	<b>318</b>	<b>257/679.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>47</b>	<b>L47</b>	<b>196</b>	<b>46 and (flex flexible fold\$4 crease\$1 sheet\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>48</b>	<b>L48</b>	<b>185</b>	<b>47 not (1 12 16 18 22 28 35 40 45)</b>	<b>USP AT; US-P GPU B</b>
<b>49</b>	<b>L49</b>	<b>179</b>	<b>46 and (tape film)</b>	<b>USP AT; US-P GPU B</b>
<b>50</b>	<b>L50</b>	<b>59</b>	<b>49 not 48</b>	<b>USP AT; US-P GPU B</b>
<b>51</b>	<b>L51</b>	<b>26</b>	<b>("4218701"   "4502903"   "4621035"   "4814943"   "4903169"   "5018005"   "5136365"   "5295044"   "5321240"   "5327010"   "5336931"   "5350645"   "5362652"   "5386342"   "5389738"   "5416358"   "5422514"   "5477047"   "5528222"   "5536466"   "5558679"   "5601941"   "5605547"   "5612513"   "5646828"   "5866441").PN.</b>	<b>USP AT</b>



	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>52</b>	<b>L52</b>	<b>2185</b>	<b>(235/488,489,491,492). ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>53</b>	<b>L53</b>	<b>1027</b>	<b>52 and (flex flexible fold\$4 sheet\$1 crease\$1 tape film)</b>	<b>USP AT; US-P GPU B</b>
<b>54</b>	<b>L54</b>	<b>945</b>	<b>53 not (1 12 16 18 22 28 35 40 45 48 50)</b>	<b>USP AT; US-P GPU B</b>
<b>55</b>	<b>L55</b>	<b>308</b>	<b>54 and paper</b>	<b>USP AT; US-P GPU B</b>